

UTILITY PATENT APPLICATION TRANSMITTAL <small>(Only for new nonprovisional applications under 37 CFR 1.53(b))</small>	Attorney Docket No. 244824US2	
	First Inventor or Application Identifier	Tamio IKEHASHI, et al.
	Title	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents	ADDRESS TO: Commissioner for Patents Mail Stop Patent Application Alexandria, Virginia 22313
1. <input checked="" type="checkbox"/> Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing) 2. <input checked="" type="checkbox"/> Specification Total Sheets <input type="text" value="31"/> 3. <input checked="" type="checkbox"/> Drawing(s) (35 U.S.C. 113) Total Sheets <input type="text" value="21"/> 4. <input type="checkbox"/> Oath or Declaration Total Pages <input type="text"/> a. <input type="checkbox"/> Newly executed (original or copy) b. <input type="checkbox"/> Copy from a prior application (37 C.F.R. §1.63(d)) (for continuation/divisional with box 17 completed) i. <input type="checkbox"/> DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b). 5. <input type="checkbox"/> CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix) 6. <input type="checkbox"/> Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary) a. <input type="checkbox"/> Computer Readable Form (CRF) b. Specification or Sequence Listing on : i. <input type="checkbox"/> CD-ROM or CD-R (2 copies); or ii. <input type="checkbox"/> Paper c. <input type="checkbox"/> Statements verifying identity of above copies	ACCOMPANYING APPLICATION PARTS 7. <input type="checkbox"/> Assignment Papers (cover sheet & document(s)) 8. <input checked="" type="checkbox"/> Application Data Sheet. See 37 CFR 1.76 9. <input type="checkbox"/> 37 C.F.R. §3.73(b) Statement <input type="checkbox"/> Power of (when there is an assignee) Attorney 10. <input type="checkbox"/> English Translation Document (if applicable) 11. <input checked="" type="checkbox"/> Information Disclosure <input checked="" type="checkbox"/> Copies of IDS Statement (IDS)/PTO-1449 Citations (4) 12. <input type="checkbox"/> Preliminary Amendment 13. <input checked="" type="checkbox"/> White Advance Serial No. Postcard 14. <input type="checkbox"/> Certified Copy of Priority Document(s) (if foreign priority is claimed) 15. <input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27 16. <input checked="" type="checkbox"/> Other: Statement of Relevancy

17. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below:


☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application no.:

Prior application information: Examiner: Group Art Unit:

For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

18. CORRESPONDENCE ADDRESS

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17510 US PTO
 10/698526
 11/03/03



Docket No. 244824US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Tamio IKEHASHI, et al.

SERIAL NO: New Application

FILING DATE: Herewith

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

FEE TRANSMITTAL

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313


FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	18 - 20 =	0	x \$18 =	\$0.00
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<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$290 =	\$0.00
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Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
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